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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/648,022	08/26/2003	Christopher N. Brindle	17987	3501
26794 . 75	90 12/29/2005	EXAMINER		
TYCO ELECTRONICS CORPORATION 4550 NEW LINDEN HILL ROAD, SUITE 450			WELLS, KENNETH B	
WILMINGTON		16 430	ART UNIT	PAPER NUMBER
			2816	
			DATE MAILED: 12/29/2009	5

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)		
	10/648,022	BRINDLE, CHRISTOPHER N.		
Office Action Summary	Examiner	Art Unit		
	Kenneth B. Wells	2816		
The MAILING DATE of this communical eriod for Reply	tion appears on the cover sheet wit	h the correspondence address		
A SHORTENED STATUTORY PERIOD FOR WHICHEVER IS LONGER, FROM THE MAII - Extensions of time may be available under the provisions of 3 after SIX (6) MONTHS from the mailing date of this communic. If NO period for reply is specified above, the maximum statute. Failure to reply within the set or extended period for reply will. Any reply received by the Office later than three months after earned patent term adjustment. See 37 CFR 1.704(b).	LING DATE OF THIS COMMUNIC 87 CFR 1.136(a). In no event, however, may a re- cation. Dry period will apply and will expire SIX (6) MONT by statute, cause the application to become ABA	CATION. Apply be timely filed Output THS from the mailing date of this communication. ANDONED (35 U.S.C. § 133).		
itatus				
1) Responsive to communication(s) filed of	on 16 Sentember 2005			
	☐ This action is non-final.			
3) Since this application is in condition for allowance except for formal matters, prosecution as to the m				
closed in accordance with the practice				
isposition of Claims				
4)	withdrawn from consideration.			
application Papers	•			
9) The specification is objected to by the E 10) The drawing(s) filed on is/are: a Applicant may not request that any objectio Replacement drawing sheet(s) including the) accepted or b) objected to b on to the drawing(s) be held in abeyance e correction is required if the drawing(s	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(d).		
11)☐ The oath or declaration is objected to by	y the Examiner. Note the attached	Office Action or form PTO-152.		
riority under 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for a) All b) Some * c) None of: 1. Certified copies of the priority doe 2. Certified copies of the priority doe 3. Copies of the certified copies of the application from the International * See the attached detailed Office action for	cuments have been received. cuments have been received in Ap the priority documents have been r l Bureau (PCT Rule 17.2(a)).	oplication No received in this National Stage		
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-10) Information Disclosure Statement(s) (PTO-1449 or PTO-1449 or PTO-1449)	-948) Paper No(s)	ummary (PTO-413) I/Mail Date formal Patent Application (PTO-152)		

Application/Control Number: 10/648,022

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1. Applicant's amendment filed on 9/16/05 has been received and entered in the case.

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 6-11, 13-15, 18 and 19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sasabata et al in view of Kawai.

Note figure 5 of Sasabata et al, where the FET is transistor 43, the first port is terminal 48, the second port is terminal 49, the first control signal input is the voltage applied to the gate of transistor 43 and the second control signal input is the signal applied to terminal 48. The shunt FET, though not disclosed, would have been obvious to one of ordinary skill in the art because it is well known in the art that a switch such as that included within circuit 52 is typically formed using a transmission gate structure such as that disclosed in figures 7 and 8 of Kawai. The motivation for using such a structure as that disclosed by Kawai is simply to obtain the well-known advantages associated with such a transmission gate

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structure (and also to provide a specific implementation of the switch in figure 5 of Sasabata et al which is shown generically). Once this specific implementation is made, there will be an FET shunting the gate of transistor 43 to ground (corresponding to applicants shunt transistor 46 in instant figure 3). This shunt FET in the combined Sasabata et al/Kawai circuitry will have a drain terminal directly coupled to the gate terminal of Sasabata et al's transistor 43.

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37

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CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth B. Wells whose telephone number is (571)272-1757. The examiner can normally be reached on Monday through Friday from 8:30am to 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan, can be reached at (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information

Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to

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the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free).

Kenneth B. Wells
Primary Examiner
Art Unit 2816

December 23, 2005